

(10)

Consider two different implementations, M1 and M2, of the same instruction set. There are 3 different classes of instructions in the ISA. M1 has a clock rate of 800 MHz and M2 has a clock rate of 500 MHz. CPI and instruction mix is as follows.

Class	M1 CPI	M2 CPI	M1 Usage (%)	M2 Usage (%)
A	4	1	30	70
B	6	4	50	20
C	8	6	20	10

C1 is a compiler produced by the manufacturers of M1, C2 is a compiler produced by the manufacturers of M2.

Assume that each compiler uses the same number of instructions for a given program but that the instruction mix is as described in the table above.

- 1) Using C1 on both machines, compare the performance of the two machines. Which machine is faster, and by how much?
- 2) Using C2 on both machines, compare the performance of the two machines. Which machine is faster, and by how much?
- 3) If you could mix and match compilers and machines, what configuration would you choose? Assume that costs are identical.

f = # instructions

$$1) ET_{M1} = \frac{d(4(0.3) + 4(0.5) + 2(0.2))}{8000(10^6)}$$

$$= \frac{d(1.2) + d(3.0) + d(1.6)}{8000(10^6)} = \frac{d(5.8)}{8000(10^6)} \text{ sec}$$

$$ET_{M2} = \frac{d(0.3 + 2(0.5) + 2(0.2))}{5000(10^6)} = \frac{d(3.5d)}{5000(10^6)} = \frac{3.5d}{5000(10^6)} \text{ sec}$$

$$PCif(\frac{M2}{M1}) = \frac{\frac{3.5d}{5000(10^6)}}{\frac{5.8d}{8000(10^6)}} = \frac{3.5d}{5.8d} = \frac{3.5(10^3)}{5(8.5)} = \frac{28}{29}$$

should be $\sqrt{\frac{M1}{M2}}$ so I flipper $\rightarrow 29/28$

* M2 is $\frac{29}{28}$ times faster than M1 using C1.*

$$2) ET_{M1} = \frac{0.7(4) + 0.2(6) + 0.1(8)}{8000(10^6)} = \frac{2.8 + 1.2 + 0.8}{8000(10^6)} = \frac{4.8}{8000(10^6)} \text{ sec}$$

$$ET_{M2} = \frac{0.7(1) + 0.2(4) + 0.1(6)}{5000(10^6)} = \frac{0.7 + 0.8 + 0.4}{5000(10^6)} = \frac{2.1}{5000(10^6)} \text{ sec}$$

$$\text{Put } \frac{M1}{M2} \left(\frac{1.2}{2000(10^6)} \right) \left(\frac{5}{2.1} \right) = \frac{6.0}{4.2}$$

* M2 is $\frac{6.0}{4.2}$ times faster than M1 using C2.*

over ↗